

**(1) Japanese Patent Application Laid-Open No. 5-219529 (1993)**

The above document corresponds to United States Patent No. 5,544,263,  
issued on August 6, 1996, Iwamura, "MOTION VECTOR CALCULATION  
CIRCUIT, WITH FLATNESS-JUDGEMENT CIRCUITRY, THAT SELECTS A  
5 GENERATED MOTION VECTOR FOR OUTPUT IN ACCORDANCE WITH A  
FLATNESS JUDGEMENT RESULT".